

**IN THE SPECIFICATION**

Please amend the title of the invention to read as follows:

-- A PROCESSOR FOR EXECUTING INSTRUCTIONS STORED IN A MEMORY--

Please replace the following paragraph beginning at page 30, line 23 and ending at page 31, line 9 with the following rewritten paragraph:

-- A program storing part 137 is similar to that in the processor of Example 1. However, an instruction stored in this program storing part 137 has, in addition to a plurality of bits indicating the content of an instruction, an end flag bit indicating the last instruction of the program module by setting the flag bit (it is not an essential issue whether the bit string including the end flag bit as described above is referred to as an instruction or only the bit string indicating the content of an instruction is referred to as an instruction). In other words, there is no difference in the substantial operation, for example, whether an execution unit 138 fetches the former as an "instruction" and executes the instruction ignoring the end flag bit, or fetches only the latter as an "instruction" and executes the instruction.--